



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mitsuhiro Yuasa

Group:

Serial No.:

Examiner:

Filed: February 6, 2004

For: SEMICONDUCTOR FABRICATING APPARATUS

VERIFIED TRANSLATION CERTIFICATE

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

The undersigned, of the below address, hereby verifies that I well know both the English and Japanese languages, and that the attached is a full, true literal and faithful translation into the English language of the 9 page Japanese language application which I am now advised carries U.S. Serial No. \_\_\_\_\_ effectively deposited in the United States Patent and Trademark Office on February 6, 2004.

The undersigned declares further that all statements made herein on personal knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signed this 1st day of April, 2004.

Translator's Signature:

Koichi Itsubo  
Name: Koichi Itsubo  
Address: A.AOKI, ISHIDA & ASSOCIATES  
Toranomon 37 Mori Bldg.  
3-5-1, Toranomon, Minato-ku,  
Tokyo 105-8423, Japan